



# Subject Name: ANALOG AND DIGITAL ELECTRONICS

# Prepared by (Faculty (s) Name):G.SUSMITHA

# Year and Sem, Department: II/I,CSE

# **Unit-I: DIDOE AND APPLICATIONS**

#### Important points / Definitions: (Minimum 15 to 20 points covering complete topics in that unit)

- 1. The term conductor is applied to any material that will support a generous flow of charge when a voltage source of limited magnitude is applied across its terminals.
- 2. An insulator is a material that offers a very low level of conductivity under pressure from an applied voltage source.
- **3.** A semiconductor, therefore, is a material that has a conductivity level somewhere between the extremes of an insulator and a conductor
- 4. An increase in temperature of a semiconductor can result in a substantial increase in the number of free electrons in the material.
- 5.  $1 \text{ eV} = 1.6 \text{X} 10^{-19} \text{ J}$
- 6. A semiconductor material that has been subjected to the doping process is called an extrinsic material.
- 7. This region of uncovered positive and negative ions is called the depletion region due to the depletion of carriers in this region.
- **8.** The process of removing one-half the input signal to establish a dc level is aptly called half-wave rectification.
- **9.** The dc level obtained from a sinusoidal input can be improved 100% using a process called full-wave rectification.
- **10.** The clamping network is one that will "clamp" a signal to a different dc level. The network must have a capacitor, a diode, and a resistive element, but it can also employ an independent dc supply to introduce an additional shift
- **11.** The clippers cicuits have the ability to "clip" off a portion of the input signal without distorting the remaining part of the alternating waveform.
- 12. In the reverse-bias region we have the transition- or depletion-region capacitance ( $C_T$ ), while in the forward-bias region we have the diffusion ( $C_D$ ) or storage capacitance.

# Short Questions (minimum 10 previous JNTUH Questions – Year to be mentioned) Unit-I

- 1. Define static resistance. (April/May 2018)
- 2. Write the effect of temperature on diode characteristics. (April/May 2018)
- 3. List the difference between the filters. (April/May 2018)
- 4. Define ripple factor (Nov-2017|)
- 5. Draw the diode equivalent circuit .mention the application of pn junction diode.(March 2017)
- 6. Explain how PN junction diode acts as a rectifier. (March 2017)

- 7. Explain the necessity of filter circuit after the rectifier circuit.(March 2017)
- 8. What is meant by depletion.(March 2017)
- 9. Explain the operation of pn junction biased in reverse direction. (March 2017)
- 10. Explain the harmonic components in rectifier. (March 2017)
- 11. Define Peak Inverse Voltage (March 2017)
- 12. Derive ripple factor of full wave rectifier. (March 2017)
- 13. Write short note son varactor diode (Nov-2017)

#### Long Questions (minimum 10 previous JNTUH Questions – Year to be mentioned)

#### Unit –I

- 1. Explain about the Current components in a p-n diode. (March 2017)
- 2. Sketch the piecewise linear characteristics of a diode. What are the approximate cut in voltages for silicon and germanium? (March 2017)
- 3. Define diffusion and transition capacitance of p-n junction diode. Prove that diffusion capacitance is proportional to current I. (March 2017)
- 4. Explain about L section Filters. (March 2017)
- 5. A full-wave single phase rectifier employs a pi- section filter consisting of two 4  $\mu$ F capacitances and a 20 H choke. The transformer voltage to the center tap is 300 V rms. The load current is 500 mA. Calculate the dc output voltage and the ripple voltage. The resistance of the choke is 200 $\Omega$ . (March 2017)
- 6. Derive the expression for transition capacitance of a diode. (Nov-2016)
- 7. Explain static characteristics of SCR (Nov-2017).
- 8. Qualitatively explain the static V-I characteristics of UJT. Derive the expression for transition capacitance of a diode. (Nov-2016)
- 9. Explain The Working Of Semiconductor Photo Diode.(April-2018)
- 10. Explain the dsign of full wave bridge rectifier(nov-2018)
- 11. Explain tunnel diode operation with the help of energy band diagrams.(Nov-2017)

#### Fill in the Blanks / Choose the Best: (Minimum 10 to 15 with Answers)

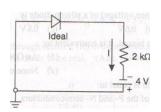
#### **Unit I: p-n Junction Diode**

1.	The conventional current in a	PN junction diode flows:		[	]
2.	<ul><li>(a) From positive to negative</li><li>(c) In the direction opposite to</li><li>The cut in voltage (or knee v</li></ul>	the electron flow. (d) Both (a) and (c) ab	ove	[	]
3.	(a) 0.2V (b) 0.6V When a diode is reverse biase		[	]	
	<ul><li>(a) An OFF switch</li><li>(c) A high resistance</li></ul>	<ul><li>(b) an ON switch</li><li>(d) none of the above</li></ul>			

4. The resistance of a diode is equal to	[	]	
<ul> <li>(a) Ohmic resistance of the P- and N- semiconductors</li> <li>(b) Junction resist</li> <li>(c) Reverse resistance</li> <li>(d) Algebraic sum of (a) a</li> <li>5. For a silicon diode, the value of the forward - bias voltage typically</li> </ul>		oove ]	
<ul> <li>(a) Must be grater than 0.3V</li> <li>(b) Must be greater than 0.7V</li> <li>(c) Depends on the width of the depletion region</li> <li>(d) Depends on the conce carriers</li> </ul>	ntration c	of major	ity
6. When forward biased, a diode		[	]
<ul> <li>(a) Blocks current</li> <li>(b) conducts current</li> <li>(c) Has a high resistance</li> <li>(d) drops a large voltage.</li> </ul> 7. A PN junction diode's dynamic conductance is directly proportional to		[	]
<ul> <li>(a) The applied voltage</li> <li>(b) the temperature</li> <li>(c) The current</li> <li>(d) the thermal voltage</li> </ul> 8. The forward region of a semiconductor diode characteristic curve is where diagonal	ode appea	ars as [	]
<ul> <li>(a) Constant current source</li> <li>(b) a capacitor</li> <li>(c) An OFF switch</li> <li>(d) an ON switch</li> <li>9. At room temperature of 25 °C, the barrier potential for silicon is 0.7 V. Its value</li> </ul>	ue at 125°	°C is[	]
(a) 0.5V (b) 0.3V (c) 0.9V (d) 0.7V 10. Junction breakdown of a PN junction occurs		[	]
<ul> <li>(a) With forward bias</li> <li>(b) with reverse bias</li> <li>(c) Because of manufacturing defect</li> <li>(d) None of these</li> <li>11. Reverse saturation current in a silicon PN junction diode nearly doubles for evolution</li> </ul>	/ery	[	]
<ul> <li>(a) 2° C rise in temperature</li> <li>(b) 5° C rise in temperature</li> <li>(c) 6° C rise in temperature</li> <li>12. The approximate value of v<sub>0</sub> across the diode shown in Fig below is</li> </ul>		[	]
(a) Zero (b) 10V (d) dependent on the value	e of R		

13. The diode D is an ideal in the circuit shown in Fig below. The current, I will be [

]



- (a) -2nA (b) zero (c) 2 mA (d) 4mA
- 14. The transition capacitance of a diode is 1nF and it can withstand a reverse potential of 400V. A capacitance of 2nF which can withstand a reverse potential of 1 kV is obtained by connecting

]

[

]

[

- (a) two 1nF diodes in series
- (b) six parallel branches with each branches comprising there 1nF diodes in series
- (c) two 1nF diodes in series
- (d) three parallel branches with each branch comprising 1nF diodes in series

#### 15. A zener diode

(a)

- has a high forward-voltage rating
- (b) has a sharp breakdown at low reverse voltage
- (c) is useful as an amplifier
- (d) has a negative resistance

#### ANSWERS

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
D	В	Α	D	В	В	С	D	А	В	D	А	С	В	В

#### **Special Purpose Electronic Devices**

- A tunnel- diode is

   (a) a very heavily-doped PN junction diode
   (b) a high resistivity PN junction diode
   (c) a slow switching device
   (d) used with reverse bias

   The light-emitting diode (LED)

   (a) is usually made from silicon
  - (b) uses a reverse-biased junction
  - (c) gives a light output which increases with the increase in temperature

	(d) depends on the recombinatio	n of holes and electrons		
3.	LED's do not require		[	]
	(a) heating	(b) warm-up time		
	(c) Both (a) and (b) above	(d) non of above		
4.	The sensitivity of a photodiode dep	pends upon	[	]
	(a) light intensity and depletio	n region width		
	(b) depletion region width and	excess carrier life time		
	(c) Excess carrier life time and	forward bias current.		
	(d) Forward bias current and li	ght intensity.		
5.	LEDs are commonly fabricated from	om gallium compounds like gallium arsenide and gallium	m phosp	hide because
	they	[	]	
	(a) Are cheap	(b) are easily available		
	(c) Emit more heat	(d) emit more light.		
6.	A LED is basically a	P-N junction.	[	]
	(a) forward-biased	(b) reverse-biased		
	(c) lightly-doped	(d) heavily-doped		
7.	As compared to a LED display, the	e distinct advantage of an LCD display is that it requires	[	]
	(a) No illumination	(b) extremely-bias		
	(c) No forward-bias	(d) a solid crystal		
8.	Before illuminating a P-N junction	photodiode, it has to be	[	]
	(a) Reverse-biased (b) for	ward-biased		
	(c) Switched ON (d) sw	itched OFF.		
9.	A LED emits visible light when its		[	]
	(a) P-N junction is reverse-biased	(b) depletion region widens		
	(c) Holes and electrons recombine	(d) P-N junction becomes hot.		
10			r	1
10.	In LED, light is emitted because		L	J
	(a) Recombination of charge carrie	rs takes place		
	(b) Diode gets heated up			
	(c) Light falling on the diode gets a	•		
11	(d) Light gets reflected due to lens	acuon.	r	1
11.	GaAs, LEDs emit radiation in the		L	]
	-	blet - blue green range of the visible region		
	(c) Visible region (d) inf	ra-red region		

#### ANSWERS

1	2	3	4	5	6	7	8	9	10	11
Α	D	С	Α	D	Α	В	Α	С	А	D

# **Rectifiers and Filters**

1.	In a half - wave rectifier, the load current flows for	[	]	
2.	<ul> <li>(a) Complete cycle of the input signal</li> <li>(b) Less than half-cycle of the input signal,</li> <li>(c) More than half-cycle but less than complete cycle of the input signal.</li> <li>(d) Only for the positive half-cycle of the input signal.</li> </ul> In a full-wave rectifier, the current in each of the diodes flows for	[	]	
	<ul> <li>(a) Complete cycle of the input signal.</li> <li>(b) Half cycle of the input signal.</li> <li>(c) Less than half of the input signal.</li> <li>(d) None of the above.</li> </ul>			
3.	The ripple factor of a bridge rectifier is	L	]	
	(a) 0.482 (b) 0.812 (c) 1.11 (d) 1.21	r		
4.	A bridge rectifier is preferable to an ordinary two-diode full-wave rectifier becaus	e [	]	
	<ul><li>(a) It needs much smaller transformer for the same output.</li><li>(b) It uses four diodes.</li><li>(c) Its transformer does not require center-tap.</li><li>(d) None of the above.</li></ul>			
5.	The basic purpose of a filter is to	[	]	
	<ul> <li>(a) Minimize variations in a.c. input signal.</li> <li>(b) Suppress harmonics in rectified output.</li> <li>(c) Remove ripples from the rectified output.</li> <li>(d) Stabilize d.c. output voltage.</li> </ul>			
6.	The use of a capacitor filter in a rectifier circuit gives satisfactory performance on	ly when the	load	
	(a) Current is high(b) current is low(c) Voltage is high(d) voltage is low	[	]	
7.	A half-wave rectifier is equivalent to a	[	]	
8.	<ul><li>(a) a clamper circuit</li><li>(b) a clipper circuit</li><li>(c) a clamper circuit with negative bias</li><li>(d) a clamper circuit with positive bias</li><li>Bridge rectifiers are preferred because</li></ul>	[	]	
	<ul><li>(a) they require small transformer</li><li>(b) they have less peak-inverse voltage</li><li>(c) they need small transformer and also have less peak-inverse voltage</li><li>(d) They have low ripple factor.</li></ul>			
9.	If $V_m$ is the peak value of an applied voltage in a half-wave rectifier with a large	e capacitor	across the l	oad,
	then the peak-inverse voltage will be	[ ]		

	(a) $\frac{V_m}{2}$	(b) <i>V<sub>m</sub></i>	(c) $\sqrt{2}V_m$	(d) $2V_m$		
10.	A voltage of 2	00 <i>cos</i> 100 <i>t</i> is	applied to a hal	f-wave rectifier with a load resistance o	f 5K. Tł	ne rectifier is
	represent by an	ideal diode in s	eries with a resis	stance of 1 K. The maximum value of cu	rrent, d.c	c. component
			urrent will be res			•
	(a) 33.33 mA, 1	10.61 mA and 16	6.67mA			
	(c) 28.33 mA, 1	14.61 mA and 13	3.33 mA	(d) 40 mA, 20 mA and 25 mA		
11.	The basic reaso	n why a full-wa	ve rectifier has a	a twice the efficiency of a half-wave recti	fier is th	at
	(a) it make	es use of transfor	mer		[	]
	(b) its ripp	le factor is much	n less			
	(c) it utilize	es both half-cyc	le of the input			
	(d) its outp	out frequency is	double the line f	requency.		
12.	The output of a	half-wave rectin	fier is suitable or	nly for	[	]
	(a) running car	radios	(b) running a.c	. motors		
	(c) charging bat	tteries	(d) running tap	be recorders.		
13.	The ripple facto	or of a bridge rec	ctifier is		[	]
	(a) 0.406	(b) 0.812	(c) 1.21 (d) 1.1	1		
14.	The ripple facto	or of a power su	pply is given by	(symbols have the usual meaning).	[	]
	(a) $\frac{P_{dc}}{P_{ac}}$	(b) $\sqrt{\left(\frac{l_{rms}}{I_{dc}}\right)^2}$ -	- 1 (c) $\sqrt{(}$	$\left(\frac{I_{dc}}{I_{rms}}\right)^2 - 1$ (d) $\frac{I_{dc}}{I_{rms}}$		
15.	The PIV of a ha	alf-wave rectifie	r circuit with a s	hunt capacitor filter is	[	]
	(a) $2V_m$ (b) $V_m$	(c) $\frac{V_m}{2}$	(d) $3V_{r}$	n		
16.	The primary fur	nction of a rectif	fier filter is to		[	]
	(a) minimi	se a.c. input var	iations			
	(b) suppres	ss odd harmonic	s in the rectifier	output		
	(c) stabilis	e d.c, level of th	e output voltage			
	(d) remove	ripples from the	e rectified outpu	t		
17.	In a rectifier, la	rger the value of	f shunt capacitor	filter	[	]
	a. larger t	he peak-to-peak	value of ripple	voltage		
	b. larger t	he peak current	in the rectifying	diode		
	c. longer t	the time that cur	rent pulse flows	through the diode		
	d. Smaller	r the d.c. voltage	e across the load			
18.	In a LC filter, t	~ ~			[	]
	(a) Increases wi	ith the load curr	ent	(b) increases with the load resistance		

(c) Remains constant with the	oad current (d) has the lowest value.		
19. The main reason why a bleede	resistor is used in a d.c. power supply is that it	[	]
(a) Keeps the supply ON	(b) improves voltage regulation		
(c) Improves filtering action	(d) both (b) and (c).		
20. Which rectifier requires four d	iodes?	[	]
(a) half-wave voltage doubler	(b) full-wave voltage doubler		
(c) full-wave bridge circuit	(d) voltage quadrupler.		
21. If, by mistake, a.c source in a b	oridge rectifier, is connected across the d.c. terminal	s, it will burn	out and
hence short di	odes.	[	]
(a) one (b) two	(c) three (d) four		
22. The d.c. output polarity from a	half-wave rectifier can be reversed by reversing	[	]
(a) The diode	(b) transformer primary		
(c) Transformer secondary	(d) both (b) and (c).		
23. In a half-wave rectifier if a rest	stance equal to load resistance is connected in paral	lel with the d	iode, then
(a) Output voltage would be ha	lved (b) output voltage would be doubled	[	]
(c) Circuit will stop rectifying	(d) output voltage will remain unchanged	,	
24. If the input supply frequency is	50 Hz, the output ripple frequency of a bridge recti	ifier is	Hz.
(a) 100 (b) 75	(c) 50 (d) 25.	[	]

# ANSWERS

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
D	В	Α	A	С	В	В	С	В	A	С	В	С	В	A	D	В	С	D	В	D	A	С	Α

# **CLIPPERS & CLAMPERS**

1. The circuit which converts sinusoidal wave form into square under some special condition is [ ]							
a) Dc restorer b) Double ended clamper c	) Attenuator	d) Clamper					
2. zener diode has temperature coefficient		[	]	]			
a) some times positive and sometimes negative	b) Only NEGI	ΓΙVΕ					
c) Both positive and negative	d) Only positiv	/e					
3. the semiconductor diode current equation is given by	у	[	]	]			
a) $I=(1-e^{V/\eta Vt})$ b) $I=I_{o}(e^{1/\eta Vt}-1)$ c) $I=I_{o}(e^{V/\eta Vt})$	-1) d) I=I <sub>0</sub>	$(1-e^{1/\eta Vt})$					

4. a comparator is a basic building block in a system used to analyze the ------ distribution of noise generated in active device [ ]

a) Both frequency and phase b) Amplitude c) Phase	d) Frequency
5. The breakdown occurring due to direct rupture of bonds because of	existence of strong electric field is
a)avalanche breakdown b) Zener breakdown c) Forward break	kdown d)none[ ]
6. The circuit which converts sinusoidal wave form into square under	some special condition is [ ]
a) Dc restorer b) Double ended clamper c) Attenua	tor d)none
7.Under steady state the output is given by, when the circuit and input	are as shown in the figure [ ]
a) $\mathbf{V}_{o} = \mathbf{V}_{i} \cdot \mathbf{V}_{m}$ b) $\mathbf{V}_{o} = \mathbf{V}_{i} + \mathbf{V}_{m}$ c) $\mathbf{V}_{o} = \mathbf{V}_{i} - \mathbf{V}_{m}$	$\mathbf{d}) \mathbf{V}_{o} = \mathbf{V}_{i} / \mathbf{V}_{m}$
8.the application of voltage comparator	[ ]
a) ohm meter b) voltmeter c)ammeter d) phasemeter	
9.time constant or RLciruit	[ ]
a) R/L b)R+L c) RL d) L/R	
10the disadvantage of shunt clipper	[ ]
a) Round shaped edges of input waveform b) No transmission	n of signal
c) Transmits same signal d) Doubles amp	litude of input waveform
11.the negative clamper is also called	[ ]
a) Positive peak clamper b) Negative peak clamper c) Positive pea	k clipper d) Negative peak clipper

ANSWERS	5:

1	2	3	4	5	6	7	8	9	10	11
D	В	С	В	В	D	С	D	D	А	А